Final Inspection and Test Operation of the SPring-8 SR Main Magnets' Power Supplies

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1. Introduction

After installation of the power supplies (PSs) of the main magnets in the SPring-8 Storage Ring (SR) in 1995, cable connections between the PSs and the magnets were completed in October of 1996. The final test operation and inspection of current stability, ripple measurement and control / interlock sequence was successfully carried out by the end of 1996.

2. Power Supplies and Cabling

For the SR main magnets, one power supply (BP)1] for bending magnets(BM), ten PSs (QP)2] for quadrupole magnets (QM) and seven PSs (SP)3] for sextupole magnets (SM) were fabricated in 1994. Also 576 StPs and 40 QAs were completed for the steering magnets and 40 quadrupole magnets4]5]6]. Installation of the power supplies at the PS rooms and cabling to the magnets was settled in 1994 and 1995. Fig. 1 shows the cabling system between the QP and the Q-magnets, with an earth line. A CV-S (shielded cross-linked polyethylene insulated) cable is used for connecting between the PS and terminal board in the accelerator tunnel. A LMFC (flame retarding, flexible insulated wires) cable is used between the terminal board and the magnets7].

Test operations for four QPs, ten QAs and two SPs with 14 cells QMs and SMs started in September 1996. The tasks of this test operation included surveying the alignment distortion of the magnets due to the heat of the cables (LMFC) under the girder of the QM and SM. During the test operation, the alignment distortion on the magnet girder was less than 30 microns with the maximum current excitation for 24 hours, and the maximum temperature of the LMFC cables was 40 ˚C

A programmable logic controller (PLC) was adopted as a magnet power supply external interlock system for the BP, QP and SP. Magnet water flow switches and coil temperature switches are connected to 24 local PLCs input modules in the maintenance hall, and concentrated interlock signals are fed to the PSs via a master PLC in the PS room-A.

3. Stability and Ripple Inspection

The final test operation, stability, ripple and power consumption measurement, control sequence and I/F (to RIO) inspections and cabling checks were done by the end of 1996. Three external DC-CT (Holec Co., Ltd. TOPACC) were adopted to the PS output cable for the measurement of long time stability. The actual current data calibrated the DAC and ADC data as a final database. Table 1 shows the measured stability and ripple ratio for BP, QP and SP. The stability was achieved better than 7 x 10^{-5} for 8 hours.

Table 2 shows the frequency spectrum of the BP and SP7 ripple. The BP current ripple was calculated from the voltage of the DC output. The SP current ripple was measured by the DC-CT and the FFT.

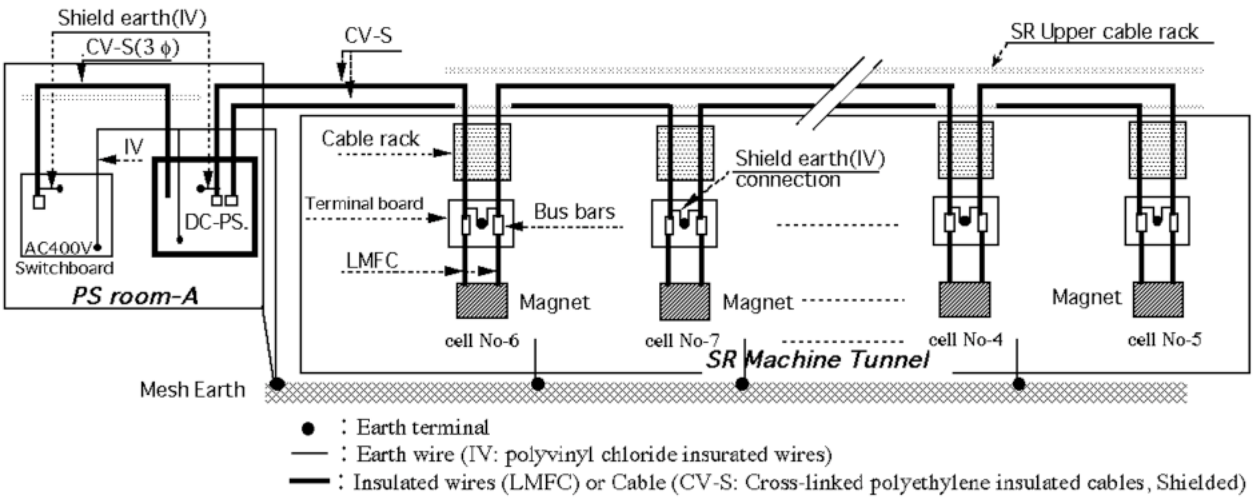

Fig. 1. The power supplies (QP, SP), magnet (QM, SM) cabling system and the earth line.

Table 1. Stability and ripple.

PS Name	Stability (8 Hr)	Ripple 60 Hz ~1 kHz
BP	0.6 * 10-5	0.8 *10-5
QP1	5.4 * 10-5	1.1 *10-5
QP2	6.3 * 10-5	0.8 *10-5
QP3	4.8 * 10-5	1.0 *10-5
QP4	7.3 * 10-5	0.8 *10-5
QP5	4.3 * 10-5	1.0 *10-5
QP6	5.6 * 10-5	1.1 *10-5
QP7	6.7 * 10-5	0.8 *10-5
QP8	7.3 * 10-5	0.7 *10-5
QP9	3.4 * 10-5	0.3 *10-5
QP10	5.3 * 10-5	1.0 *10-5
SP1	2.8 * 10-5	
SP2	7.4 * 10-5	
SP3	4.0 * 10-5	
SP4	6.8 * 10-5	
SP5	4.8 * 10-5	
SP6	7.0 * 10-5	
SP7	4.1 * 10-5	1.36E-05

For a bending magnet PS, four input transformers for 6.6 kV are located in an outer yard and connected by four sets of bus ducts with 24 phase rectifiers.

A 33.3 kHz MOS-FET switching regulator for the SP, is piggy-backed by the 12 phase thyristor regulator. This higher order ripple noise was not observed in the magnet terminals.

Table 2. Ripple frequency spectrum.

Freq. (Hz)	Meas. Ripple Ratio to 1270 A BP	300 A Max. SP7
60	4.33E-07	1.25E-05
120	2.12E-06	1.36E-05
180	5.21E-08	9.95E-06
240	7.33E-08	4.99E-06
300	7.91E-06	< 7 E-07
360	1.51E-07	1.58E-06
420	2.50E-06	< 7 E-07
480	2.76E-08	7.91E-07
720	1.86E-08	< 7 E-07
1080	1.92E-08	7.91E-06

Table 3 shows the PS specifications, power consumption for the maximum operation mode and 2-cell mode Lattice operation. An AC 6.6 kV - 400V transformer connection are also indicated in this table 3. "R8" and "R9" are the titles of the transformer (AC 6.6kV to AC400V). Each sign (+/-) of these titles indicates a phase shift of +15 or -15 degree of the QP internal transformer.

In order to reduce the harmonic current distortion, input transformer phase of the five QPs (QP-1, 3, 4, 5, 10) are shifted. The QP has 12 phase thyristor diodes and a reactor transformer with an active filter system.

To reduce a heat effect for the magnet girder, the current density of the LMFC cables was suppressed to be small. Therefore, the magnet connection cable loss

Table 3. QP and SP output specification, power consumption, and AC 400V inlet transformer connection for a maximum operation and a 2 cell mode Lattice operation.

	Designed Spec.			Actual Volt. & Power				AC Inlet		2 cell Mode Operation			Input
96.Dec.	(A)	(V)	Power	(V)	Ratio-V	Ohm	kW	KVA	Ratio	(A)	(V)	kW	Trans.
BP	1270	1157	1469	1053	0.910	0.836	1337	1627	0.82	1236	1033	1277	
QP1	410	480	192	367	0.765	0.895	150	256	0.59	220	193	42	R9+
QP2	394	918	360	821	0.894	2.084	323	483	0.67	318	632	201	R9-
QP3	410	578	237	444	0.768	1.083	182	303	0.60	-347	373	129	R9+
QP4	521	619	322	477	0.771	0.916	249	440	0.56	-487	439	214	R8+
QP5	549	726	399	568	0.782	1.035	312	497	0.63	523	535	280	R8+
QP6	549	726	399	562	0.774	1.024	309	540	0.57	523	534	279	R8-
QP7	521	619	322	476	0.769	0.914	248	440	0.56	-487	437	213	R8-
QP8	562	743	417	584	0.786	1.039	328	554	0.59	-451	460	208	R9-
QP9	569	1281	728	1123	0.877	1.974	639	947	0.67	498	970	483	R8
QP10	558	619	345	484	0.782	0.867	270	456	0.59	-376	313	118	R9+
SP1	300	650	195	590	0.908	1.967	177	254	0.70	232	455	106	R8
SP2	300	650	195	590	0.908	1.967	177	250	0.71	-266	506	134	R8
SP3	300	650	195	590	0.908	1.967	177	248	0.71	-244	465	113	R9
SP4	300	750	225	680	0.907	2.267	204	288	0.71	256	578	148	R9
SP5	300	650	195	590	0.908	1.967	177	252	0.70	-244	467	114	R9
SP6	300	650	195	590	0.908	1.967	177	251	0.71	-146	287	42	R9
SP7	300	650	195	590	0.908	1.967	177	254	0.70	256	510	131	R9

became smaller. The QM coil resistance and the output voltage was achieved smaller than the designed value. In contrast, the power factors increased due to a decreasing output voltage. Accordingly, the QP input voltage (AC 420V) was decreased 5% by changing the transformer tap.

Currents for the QMs in the long straight sections are compensated by an auxiliary power supply system (QA). This current stability and ripple were better than 2 x 10^{-4}.

Thirty-six units of the St-PS are mounted in one cubicle and three DC sources (250V) supply every 12 units. Each unit has a DC-DC switching regulator system (100 kHz). The QA is a similar type but with a floating (isolated) system.

576 sets of steering magnet PS's were tested. And the current stability and ripple were better than 1.8 x 10^{-4}.

4. Remote Control

The PSs of the SR magnets are controlled by a VME - RIO system[8][9][10]. The RIO type-A is used for StP, QA. It has a 16 bits DAC (Digital to Analog Converter) and a double integration type ADC (Analog to Digital Converter), which monitors the actual current using a shunt resistance output. This ADC has an accuracy of 1 x 10^{-5}.

The reference voltage for the large PS current control is given by a 16 bit DAC, controlled by the digital output of the RIO type-B. These DAC's for the BP, QP and SP are installed in a temperature controlled box in the PS cubicle.

The ADC data is transferred to the RIO card via a 16 bits parallel signal. A cyclic time for ADC read out and status read out for the 22 RIO slave cards (for the BP, QP, SP and QP's bypass) is 5 ms. A VME controlled remote I/O card is connected to this PS using an optical fiber cable.

The current set step time with a DAC was 50 or 60 ms. Fig. 2 shows a time chart of DAC data set and strobe for BP, QP and SP. The DAC set step time is within 60 ms. PS control programs (Equipment Manager in a VME computer, and control panels with a Graphical User Interface) were created and tested.

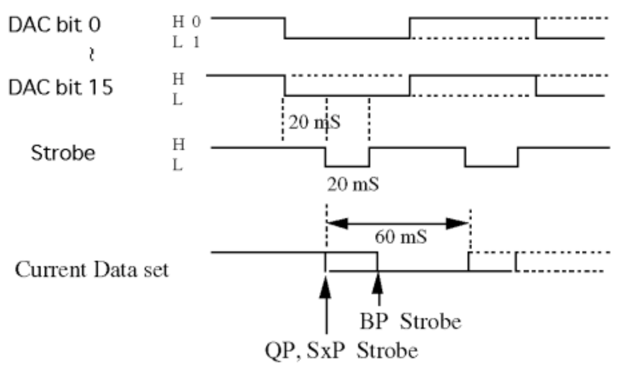

Fig. 2. Time chart of DAC data and strobe for BP, QP and SP.

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